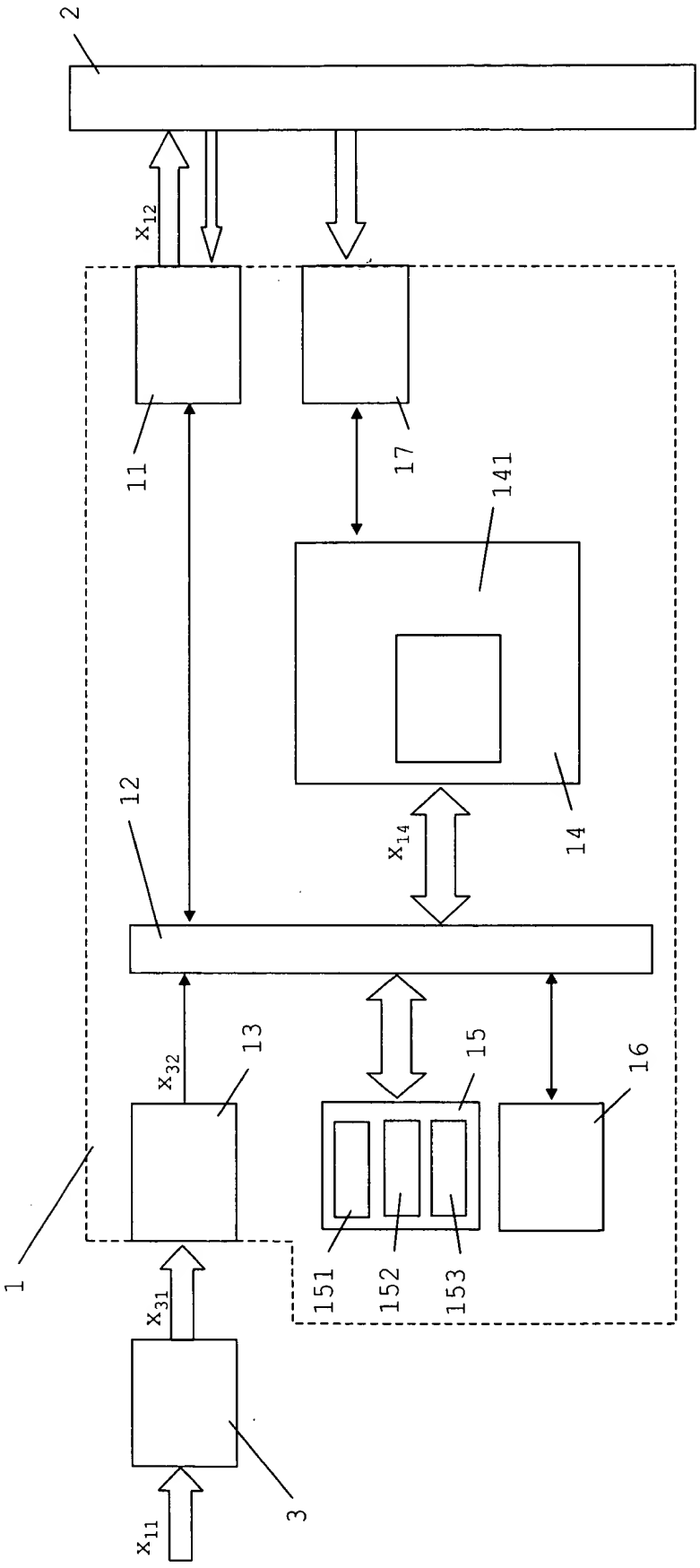

Fig. 1

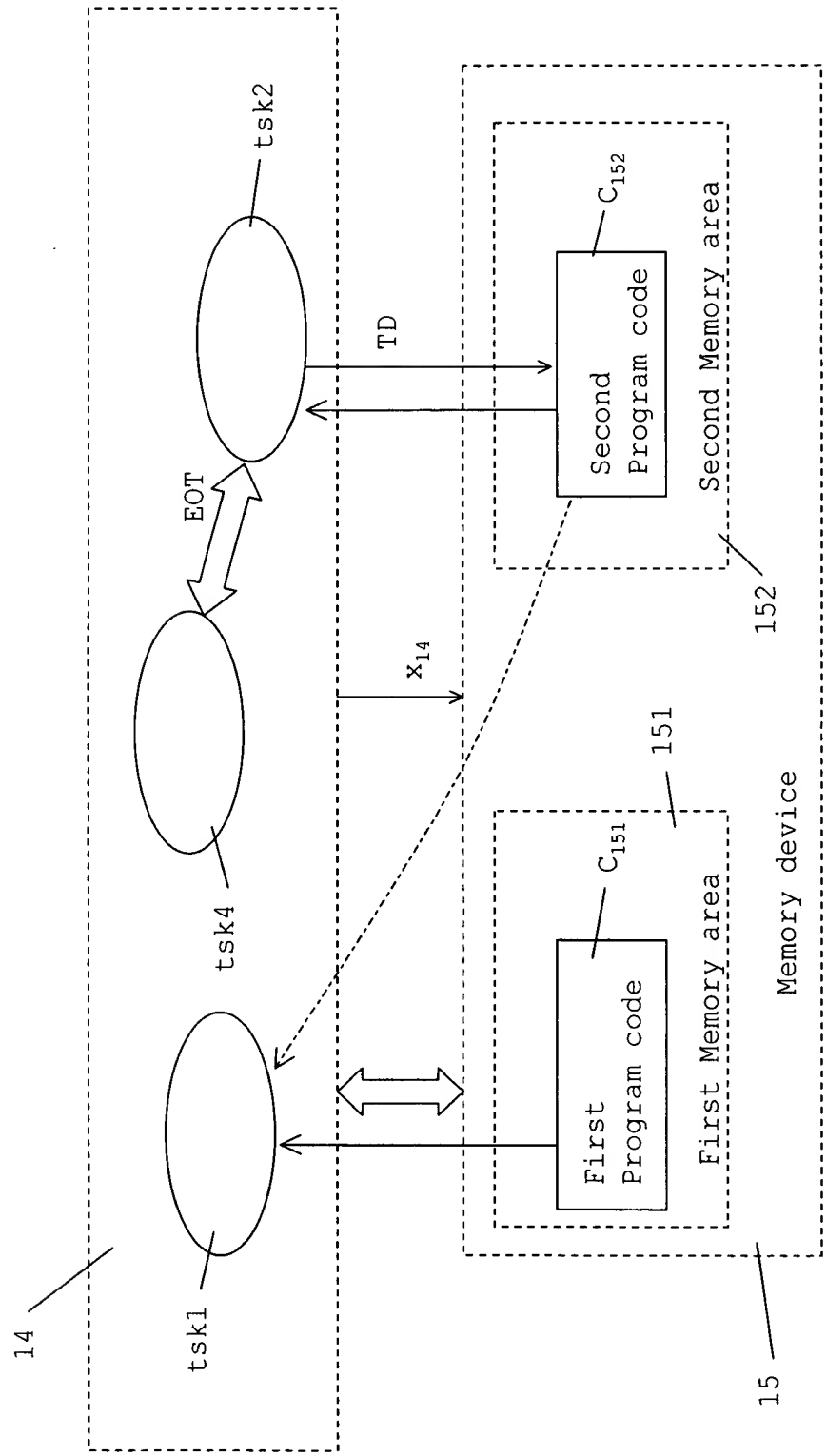
14
tsk1
tsk4
tsk2
EOT
TD
x_{14}
15
Memory device
151
First Memory area
First Program code
C_{151}
152
Second Memory area
Second Program code
C_{152}

Fig. 2

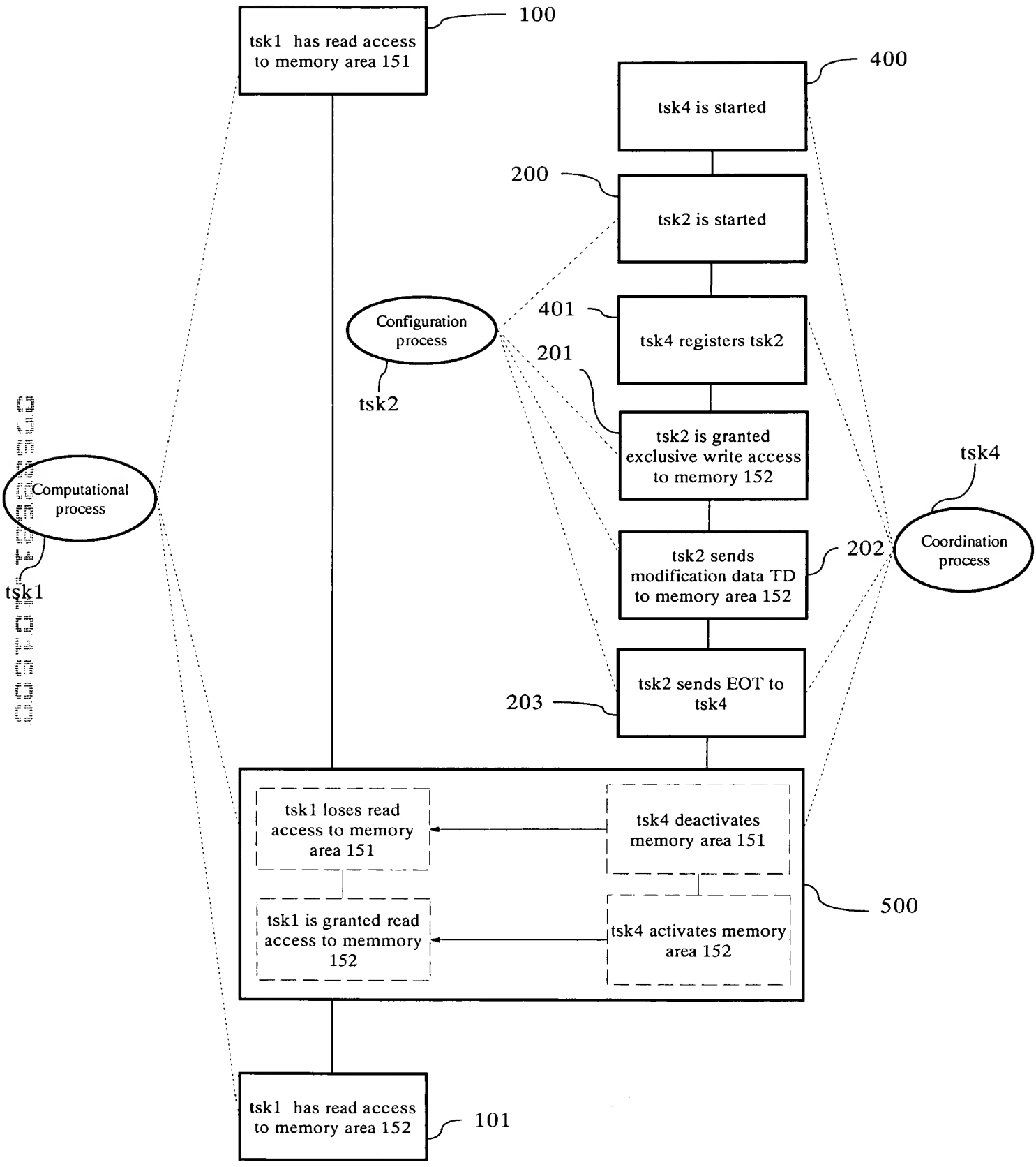
100
tsk1 has read access to memory area 151
400
tsk4 is started
200
tsk2 is started
Configuration process
tsk2
401
tsk4 registers tsk2
201
tsk2 is granted exclusive write access to memory 152
tsk4
Computational process
tsk1
tsk2 sends modification data TD to memory area 152
202
Coordination process
tsk2 sends EOT to tsk4
203
tsk1 loses read access to memory area 151
tsk4 deactivates memory area 151
tsk1 is granted read access to memmory 152
tsk4 activates memory area 152
500
tsk1 has read access to memory area 152
101

Fig. 3

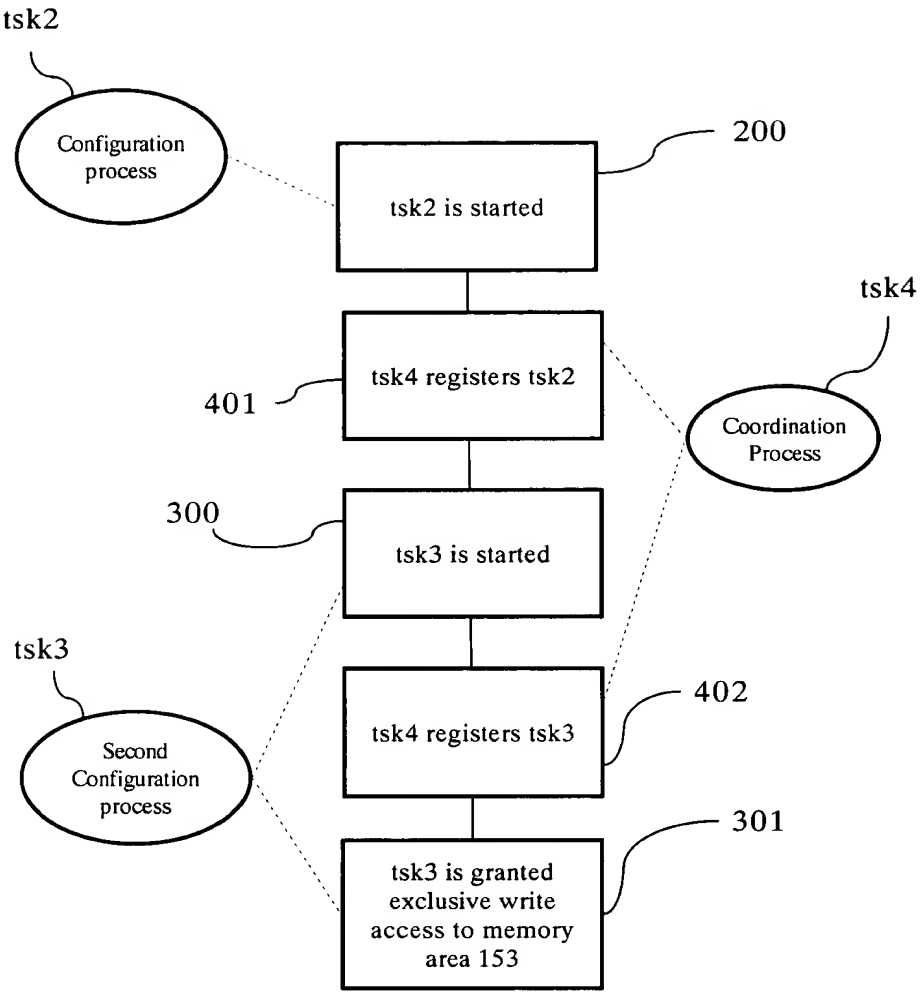
tsk2
Configuration process
200
tsk2 is started
tsk4
401
tsk4 registers tsk2
Coordination Process
300
tsk3 is started
tsk3
Second Configuration process
402
tsk4 registers tsk3
301
tsk3 is granted exclusive write access to memory area 153

Fig. 4